## CSci 127: Introduction to Computer Science



Finished the lecture preview?
hunter.cuny.edu/csci

## Guest Speakers

- Today we will start with guest speakers from computer science clubs at Hunter
- Instead of the usual lecture slip, you will fill out a survey to get credit for today's lecture (link on Blackboard under Course Materials)


## Challenge (Group Work):

- Basic pattern: img[rows, columns, channels] with: start:stop:step.
- Assuming the libraries are imported, what do the following code fragments produce:

```
- img = np.ones( (10,10,3) )
    img[0:10,0:5,0:2] = 0
```


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- num $=$ int (input('Enter size '))
img $=n p$.ones ( (num,num,3) )
$\operatorname{img}[:: 2,:, 1:]=0$


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## Challenge (Group Work)

- Basic pattern: img[rows, columns, channels] with: start:stop:step.
- Assuming the libraries are imported, what do the following code fragments produce:

```
- img = np.zeros( (8,8,3) )
    img[::2,1::2,0] = 1
```


## Challenge (Group Work)

- Basic pattern: img[rows, columns, channels] with: start:stop:step.
- Assuming the libraries are imported, what do the following code fragments produce:
- img $=$ np.zeros $((8,8,3))$
$\operatorname{img}[:: 2,1:: 2,0]=1$



## Today's Topics

- Recap: Decisions
- Logical Expressions
- Circuits
- Binary Numbers


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## Decisions



Fig: Operation of if...elif...else statement

## Side Note: Reading Flow Charts


(xkcd/518)

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## Logical Operators

and

| in1 |  | in2 | returns: |
| :--- | :--- | :--- | :--- |
| False | and | False | False |
| False | and | True | False |
| True | and | False | False |
| True | and | True | True |

## Logical Operators

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| False | and | True | False |
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| True | and | True | True |
| or |  |  |  |
| in1 |  | in2 | returns: |
| False | or | False | False |
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## Circuit Demo


(Demo with circuitverse)

## Challenge

Predict when these expressions are true:


- in1 or not in1:

- not(in1 or in2):
- (in1 and in2) and in3:



## Circuit Demo


(Demo with circuitverse)


## Challenge



Draw a circuit that corresponds to each logical expression:

- in1 or in2
- (in1 or in2) and (in1 or in3)
- (not(in1 and not in2)) or (in1 and (in2 and in3))


## Circuit Demo


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- Computers store numbers using the Binary system (base 2)


## Binary Numbers

- Logic $\rightarrow$ Circuits $\rightarrow$ Numbers
- Digital logic design allows for two states:
- True / False
- On / Off (two voltage levels)
- 1 / 0
- Computers store numbers using the Binary system (base 2)
- A bit (binary digit) being 1 (on) or 0 (off)


## Binary Numbers



Example: $1 \times 16+1 \times 8+1 \times 1=16+8+1=25$

- Two digits: $\mathbf{0}$ and $\mathbf{1}$


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- In each position the digit is either 0 or 1 , so given a binary number we can obtain the decimal equivalent as follows:


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- In the "twos" position we either have a 2 or not


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- In the "ones" position we either have a 1 or not
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- In the "fours" position we either have a 4 or not ...


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- Example:

$$
11001_{\text {base } 2}=16+8+1=25_{\text {base } 10}
$$

## Recap



- In Python, we introduced:


## Recap

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- Decisions
- Logical Expressions
- Circuits
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## Final Exam



- Since you must pass the final exam to pass the course, we end every lecture with final exam review.
- Pull out something to write on (not to be turned in).
- Lightning rounds:
- write as much you can for 60 seconds;
- followed by answer; and
- repeat.
- Past exams are on the webpage (under Final Exam Information).


## Weekly Reminders!



Before next lecture, don't forget to:

- Work on this week's Online Lab


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- If you need help, schedule an appointment for Tutoring in lab 1001G $11 a m-5 p m$
- Take the Lecture Preview on Blackboard on Monday (or no later than 10am on Tuesday)


## Lecture Slips \& Writing Boards



- Hand your lecture slip to a UTA.
- Return writing boards as you leave.

